

Digital Logic Circuit Analysis & Design

Second Edition



Victor P. Nelson
Bill D. Carroll
H. Troy Nagle
J. David Irwin



Digital Logic Circuit Analysis And Design

Tertulien Ndjountche



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Digital Logic Circuit Analysis and Design ,1995

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Digital Logic Circuit Analysis and Design [rental Edition] Victor P Nelson,Bill D Carroll,H Troy Nagle,David Irwin,2020-02-18 This print textbook is available for students to rent for their classes The Pearson print rental program provides students with affordable access to learning materials so they come to class ready to succeed Balance breadth and depth of coverage with practical real world design methods Digital Logic Circuit Analysis and Design provides an authoritative state of the art approach to the fundamentals of digital logic analysis and design that is highly supportive of student learning The book balances theory and practice in depth without getting bogged down in excessive technical or mathematical language Retaining its tradition of both clarity and rigor the 2nd Edition features extensive coverage of current topics of interest such as modeling with Verilog and VHDL design with programmable devices and computer aided design Filled with updated illustrations examples and problems this text helps students gain a solid sense of how theory underlies practice This title is also available digitally as a standalone Pearson eText Contact your Pearson rep for more information

Digital Logic Circuit Analysis and Design Victor Peter Nelson,H. Troy Nagle,Bill D. Carroll,J. David Irwin,2019 The advent in the 1980s of low cost easy to use computers such as the IBM Personal Computer and the Apple II led to decades of expanding applications of computers in all aspects of life Later the Internet made it feasible to interconnect computers around the world which spurred even more uses of computers including cloud computing The continued miniaturization and cost reduction of microelectronics has resulted in the proliferation of mobile devices an emergence of the Internet of Things IoT and the rise of on chip parallel processing Continued evolution of computer hardware coupled with advances in artificial intelligence and software will lead to even more sophisticated applications in the years to come

Digital Logic Circuits Dr. P. Kannan, Mrs. M. Saraswathi, Mr. C. Rameshkumar, PREFACE OF THE BOOK This book is extensively designed for the third semester EEE EIE students as per Anna university syllabus R 2013 The following chapters constitute the following units Chapter 1 9 covers Unit 1 Chapter 2 and 3 covers Unit 2 Chapter 4 and 5 covers Unit 3 Chapter 6 and 7 covers Unit 4 Chapter 8 VHDL Unit 5 CHAPTER 1 Introduces the Number System binary arithmetic and codes CHAPTER 2 Deals with Boolean algebra simplification using Boolean theorems K map method Quine McCluskey method logic gates implementation of switching function using basic Logical Gates and Universal Gates CHAPTER 3 Describes the combinational circuits like Adder Subtractor Multiplier Divider magnitude comparator encoder decoder code converters Multiplexer and Demultiplexer CHAPTER 4 Describes with Latches Flip Flops Registers and Counters CHAPTER 5 Concentrates on the Analysis as well as design of synchronous sequential circuits Design of synchronous counters sequence generator and Sequence detector CHAPTER 6 Concentrates the Design as well as Analysis of Fundamental Mode circuits Pulse mode Circuits Hazard Free Circuits ASM Chart and Design of Asynchronous counters CHAPTER 7 Discussion on memory devices which includes ROM RAM PLA PAL Sequential logic devices and ASIC CHAPTER 8 The chapter concentrates on the design fundamental building blocks Data types operates subprograms packages compilation process used for VHDL It discusses on Finite state machine as an important tool for designing logic level state machines The chapter also discusses register transform level designing and test benches usage in stimulation of the state logic machines CHAPTER 9 Concentrate on the comparison operation and characteristics of RTL DTL TTL ECL and MOS families We have taken enough care to present the definitions and statements of basic laws and theorems problems with simple steps to make the students familiar with the fundamentals of Digital Design

Digital Logic Circuit Analysis and Design (second Edition) Victor Peter Nelson, Bill D. Carroll, H. Troy Nagle, J. David Irwin, 2020

Digital Circuit Analysis and Design with Simulink Modeling and Introduction to CPLDs and FPGAs Steven T. Karris, 2007 This book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits It is self contained

Digital Circuit Analysis and Design, 2005

CMOS Logic Circuit Design John P. Uyemura, 2007-05-08 This is an up to date treatment of the analysis and design of CMOS integrated digital logic circuits The self contained book covers all of the important digital circuit design styles found in modern CMOS chips emphasizing solving design problems using the various logic styles available in CMOS

Practical Design of Digital Circuits Ian Kampel, 2015-08-11 Practical Design of Digital Circuits Basic Logic to Microprocessors demonstrates the practical aspects of digital circuit design The intention is to give the reader sufficient confidence to embark upon his own design projects utilizing digital integrated circuits as soon as possible The book is organized into three parts Part 1 teaches the basic principles of practical design and introduces the designer to his tools or rather the range of devices that can be called upon Part 2 shows the designer how to put these together into viable designs It includes two detailed descriptions of actual design exercises The first of these is a fairly simple exercise in CMOS design the second is a much more complex design for an electronic game using TTL devices Part 3 focuses on microprocessors It illustrates how a particular design problem changes emphasis when a microprocessor is introduced This book is aimed at a fairly broad market it is intended to aid the linear design engineer to cross the barrier into digital electronics it should provide interesting supporting reading for students studying digital electronics from the more academic viewpoint and it should enable the enthusiast to design much more ambitious and sophisticated projects than he could otherwise attempt if restricted to linear devices

Digital Integrated Circuits John E. Ayers, 2018-09-03 Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years The second edition of Digital Integrated Circuits Analysis and Design focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication this book delves into the dramatic advances in the field including new applications and changes in the physics of operation made possible by relentless miniaturization This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic Like the first edition this volume is a crucial link for integrated circuit engineers and those studying the field supplying the cross disciplinary connections they require for guidance in more advanced work For pedagogical reasons the author uses SPICE level 1 computer simulation models but introduces BSIM models that are indispensable for VLSI design This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models With four new chapters more than 200 new illustrations numerous worked examples case studies and support provided on a dynamic website this text significantly expands concepts presented in the first edition

DIGITAL LOGIC DESIGN Sonali Singh, 2018-06-01 Description The book is an attempt to make Digital Logic Design easy and simple to understand The book covers various features of Logic Design using lots of examples and relevant diagrams The complete text is reviewed for its correctness This book is an outcome of sincere effort and hard work to bring concepts of Digital Logic Design close to the audience of this book The salient features of the book Easy explanation of Digital System and Binary Numbers with lots of solved examples Detailed covering of Boolean Algebra and Gate Level Minimization with proper examples and diagrammatic representation Detailed analysis of different Combinational Logic Circuits Complete Synchronous sequential Logic understanding Deep understanding of Memory and Programmable Logic Detailed analysis of different Asynchronous Sequential Logic Table Of Contents Unit 1 Digital System and Binary Numbers Part 1 Digital System and Binary Numbers Part 2 Boolean Algebra and Gate Level Minimization Unit 2 Combinational Logic Unit 3 Sequential Circuits Unit 4 Memory Programmable Logic and Design Unit 5 Asynchronous Sequential Logic

Digital Logic Techniques John Stonham, 2017-11-22 The third edition of Digital Logic Techniques provides a clear and comprehensive treatment of the representation of data operations on data combinational logic design sequential logic computer architecture and practical digital circuits A wealth of exercises and worked examples in each chapter give students valuable experience in applying the concepts and techniques discussed Beginning with an objective comparison between analogue and digital representation of data the author presents the Boolean algebra framework for digital electronics develops combinational logic design from first principles and presents cellular logic as an alternative structure more relevant than canonical forms to VLSI implementation He then addresses sequential logic design and develops a strategy for designing finite state machines giving students a solid foundation for more advanced studies in automata theory The second half of the book focuses on the digital system as an entity Here the author examines the implementation of logic systems in programmable hardware outlines the specification of a system explores arithmetic processors and elucidates fault diagnosis The final chapter examines the electrical properties of logic components compares the different logic families and highlights the problems that can arise in constructing practical hardware systems

Digital Logic Circuits using VHDL Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 The book is written for an undergraduate course on digital electronics. The book provides basic concepts, procedures, and several relevant examples to help the readers understand the analysis and design of various digital circuits. It also introduces hardware description language VHDL. The book teaches you the logic gates, logic families, Boolean algebra, simplification of logic functions, analysis and design of combinational circuits using SSI and MSI circuits, and analysis and design of the sequential circuits. This book provides in-depth information about multiplexers, demultiplexers, decoders, encoders, circuits for arithmetic operations, various types of flip-flops, counters, and registers. It also covers asynchronous sequential circuits, memories, and programmable logic devices.

Digital Electronics 2 Tertulien Ndjountche, 2016-08-29 As electronic devices become increasingly prevalent in everyday life, digital circuits are becoming even more complex and smaller in size. This book presents the basic principles of digital electronics in an accessible manner, allowing the reader to grasp the principles of combinational and sequential logic and the underlying techniques for the analysis and design of digital circuits. Providing a hands-on approach, this work introduces techniques and methods for establishing logic equations and designing and analyzing digital circuits. Each chapter is supplemented with practical examples and well-designed exercises with worked solutions. This second of three volumes focuses on sequential and arithmetic logic circuits. It covers various aspects related to the following topics: latch and flip-flop, binary counters, shift registers, arithmetic and logic circuits, digital integrated circuit technology, semiconductor memory, programmable logic circuits. Along with the two accompanying volumes, this book is an indispensable tool for students at a bachelor's or master's level seeking to improve their understanding of digital electronics and is detailed enough to serve as a reference for electronic automation and computer engineers.

Digital Logic Design Guy Even, Moti Medina, 2012-10-08 This textbook, based on the authors' fifteen years of teaching, is a complete teaching tool for turning students into logic designers in one semester. Each chapter describes new concepts, giving extensive applications and examples. Assuming no prior knowledge of discrete mathematics, the authors introduce all background in propositional logic, asymptotics, graphs, hardware, and electronics. Important features of the presentation are: All material is presented in full detail. Every designed circuit is formally specified and implemented; the correctness of the implementation is proved, and the cost and delay are analyzed. Algorithmic solutions are offered for logical simulation, computation of propagation delay, and minimum clock period. Connections are drawn from the physical analog world to the digital abstraction. The language of graphs is used to describe formulas and circuits. Hundreds of figures, examples, and exercises enhance understanding. The extensive website <http://www.eng.tau.ac.il/guy> includes teaching slides, links to Logisim, and a DLX assembly simulator.

Analysis and Design of Digital Integrated Circuits David A. Hodges, Horace G. Jackson, Resve A. Saleh, 2003 The third edition of Hodges and Jackson's *Analysis and Design of Digital Integrated Circuits* has been thoroughly revised and updated by a new co-author, Resve Saleh of the University of British Columbia. The new edition combines the approachability and concise nature of the Hodges and Jackson classic with a complete overhaul to bring the book into the 21st century. The new edition has replaced the emphasis on BiPolar with an emphasis on CMOS. The outdated MOS transistor model used throughout the book will be replaced with the now standard deep submicron model. The material on memory has been expanded and updated. As well, the book now includes more on SPICE simulation and new problems that reflect recent technologies. The emphasis of the book is on design, but it does not neglect analysis and has as a goal to provide enough information so that a student can carry out analysis as well as be able to design a circuit. This book provides an excellent and balanced introduction to digital circuit design for both students and professionals.

Design, Analysis and Test of Logic Circuits Under Uncertainty Smita Krishnaswamy,Igor L. Markov,John P. Hayes,2012-09-21 Logic circuits are becoming increasingly susceptible to probabilistic behavior caused by external radiation and process variation In addition inherently probabilistic quantum and nano technologies are on the horizon as we approach the limits of CMOS scaling Ensuring the reliability of such circuits despite the probabilistic behavior is a key challenge in IC design one that necessitates a fundamental probabilistic reformulation of synthesis and testing techniques This monograph will present techniques for analyzing designing and testing logic circuits with probabilistic behavior

Digital Logic and Microprocessor Design with VHDL Enoch O. Hwang,2006 This book will teach students how to design digital logic circuits specifically combinational and sequential circuits Students will learn how to put these two types of circuits together to form dedicated and general purpose microprocessors This book is unique in that it combines the use of logic principles and the building of individual components to create data paths and control units and finally the building of real dedicated custom microprocessors and general purpose microprocessors After understanding the material in the book students will be able to design simple microprocessors and implement them in real hardware

Digital Electronics 1 Tertulien Ndjountche,2016-06-17 The omnipresence of electronic devices in our everyday lives has been accompanied by the downscaling of chip feature sizes and the ever increasing complexity of digital circuits This book is devoted to the analysis and design of digital circuits where the signal can assume only two possible logic levels It deals with the basic principles and concepts of digital electronics It addresses all aspects of combinational logic and provides a detailed understanding of logic gates that are the basic components in the implementation of circuits used to perform functions and operations of Boolean algebra Combinational logic circuits are characterized by outputs that depend only on the actual input values Efficient techniques to derive logic equations are proposed together with methods of analysis and synthesis of combinational logic circuits Each chapter is well structured and is supplemented by a selection of solved exercises covering logic design practices

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